

Patent Application
Attorney Docket No.: 57941.000016
Client Reference No.: RA262.CIP2.US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: :
:
Anthony BESSIOS et al. :
: Group Art Unit: Unassigned
Appln. No.: 10/667,355 :
: Examiner: Unassigned
Filed: September 23, 2003 :
:
For: TECHNIQUE FOR UTILIZING SPARE :
BANDWIDTH RESULTING FROM THE :
USE OF A TRANSITION-LIMITING :
CODE IN A MULTI-LEVEL :
SIGNALING SYSTEM :

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the duty under 37 C.F.R. § 1.56 of each individual associated with the filing and prosecution of the above-identified patent application (hereinafter, "associated individuals") to disclose all information known to that individual to be material to patentability, Applicant(s) hereby submits attached Form PTO-1449 (modified) listing cited references. This submission is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure.

The cited references, while believed to be of some relevance, are not necessarily considered to teach or suggest any aspect of the invention described and claimed in the above-identified patent application. Applicant(s) hereby expressly reserves the right to swear behind the effective dates of any of the cited references. Applicant(s) further reserves the right to question the relevance, materiality, and/or prior art status of any of the cited references in whole, in part, or in combination, subsequent to the filing of this information disclosure statement. This information disclosure statement is also not to be construed as a representation that a search has, or has not, been conducted or that no better art exists. Rather, this information disclosure statement discloses only the best references of which the associated individuals are aware.

The Examiner is respectfully requested to consider each of the cited references, to indicate such consideration by initialing in the space provided next to each cited reference on the enclosed Form PTO-1449 (modified), to sign the initialed Form PTO-1449 (modified), and to return a copy of the same with the next communication to the Applicant(s).

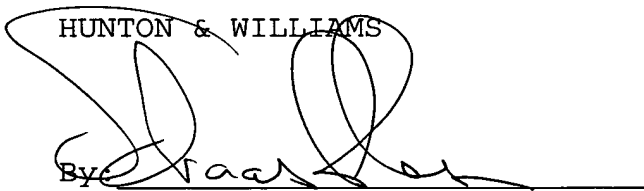
Since copies of the cited references were previously submitted in prior U.S. Patent Application No. 10/314,985, copies of the cited references are not being submitted herewith.

However, copies will be forwarded at the request of the Examiner.

In accordance with 37 CFR § 1.97(b), this information disclosure statement is being filed (i) within three months of the filing date of the above-identified patent application; (ii) within three months of the date upon which the above-identified patent application entered the national stage as set forth in 37 CFR § 1.491; or (iii) before the mailing date of a first Office Action on the merit for the above-identified patent application. Accordingly, no statement or fee is required.

Please charge any shortage in fees due in connection with the filing of this communication to Deposit Account No. 50-0206, and please credit any excess fees to such deposit account.

Respectfully submitted,

HUNTON & WILLIAMS

By: _____
Thomas E. Anderson
Registration No. 37,063

TEA/sdw
Hunton & Williams
1900 K Street
Washington, DC 20006
Telephone: (202) 955-1808
Facsimile: (202) 778-2201
Date: December 23, 2003

FORM PTO-1449 (REV. 7-80) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF MATERIALS CITED BY APPLICANT (Use several sheets if necessary)	ATTY. DOCKET NO.: 57941.000016	SERIAL NO.: 10/667,355
	INVENTOR'S NAME: Anthony BESSIOS et al.	EXAMINER: Unassigned
	FILING DATE: September 23, 2003	GROUP: Unassigned

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	U1.	3754237	08/21/1973	de Laage de Meux			
	U2.	4408189	10/04/1983	Betts et al.			
	U3.	4486739	12/04/1984	Franaszek et al.			
	U4.	4495626	01/22/1985	Brunin et al.			
	U5.	4665517	05/12/1987	Widmer			
	U6.	4667337	05/19/1987	Fletcher			
	U7.	4855742	08/08/1989	Verboom			
	U8.	5142167	08/25/1992	Temple et al.			
	U9.	5655078	05/05/1997	Anderson et al.			
	U10.	5903231	05/11/1999	Emelko			
	U11.	5999571	12/07/1999	Shin et al.			
	U12.	6005895	12/21/1999	Perino et al.			
	U13.	6018304	01/25/2000	Bessios			
	U14.	6067326	05/23/2000	Jonsson et al.			
	U15.	6078627	06/20/2000	Crayford			
	U16.	6094461	07/25/2000	Heron			
	U17.	6226330	05/01/2001	Mansur			
	U18.	6324602	11/27/2001	Chen et al.			

OTHER MATERIALS (Including Author, Title, Date, Pertinent Pages, Etc.)

U18.	MRCEA R. STAN et al., "Coding a terminated bus for low power," Great Lakes Sympon VLSI, March, 1995, pp. 70-73
U19.	MRCEA R. STAN, "Bus-Invert coding for low power I/O," IEEE Transactions On Very Large Scale Integration (VLSI) Systems, Vol. XX, No. Y, 1999, pp. 100-108
U20.	KAZUYUKI NAKAMURA et al., "A 500-MHz 4-Mb CMOS pipeline-burst cache SRAM with point-to-point noise reduction coding I/O
U21.	DAVID D. FALCONER et al., "Bounds on error-pattern probabilities for digital communications systems," IEEE Transactions Communications, Vol. COM-20, No. 2, April, 1972, pp. 132-139
U22.	RAMIN FARJAD-RAD et al., "A 0.3- μ m CMOS 8-Gb/s 4-PAM serial link transceiver," IEEE Journal of Solid-State Circuits, Vol. 35, No. 5, May, 2000
U23.	A. X. WIDMER et al., "A dc-balanced, partitioned-block, 8B/10B transmission code," pp. 440-451

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.